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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:
Anderson, et al.

Serial No.: 10/710,006

Filed: June 11, 2004

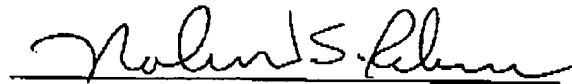
Group Art Unit: 2811

Examiner: Arena, Andrew Owens

Atty. Docket No.: BUR920040019US1

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Mohammad S. Rahman

For: METHOD AND STRUCTURE FOR PROVIDING TUNED LEAKAGE CURRENT IN
CMOS INTEGRATED CIRCUITS

AMENDMENT UNDER 37 C.F.R. §1.111

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This amendment is in response to the Office Action mailed July 14, 2005, setting a three-month statutory period for response. Therefore, this amendment is timely filed. Please amend the above-identified patent application as follows:

10/710,006

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